

L Number	Hits	Search Text	DB	Time stamp
13	117	(32-bit 4-BIT 8-BIT 16-BIT 64-BIT 128-BIT 256-BIT 512-BIT) adj memory and word-and address and word with row and column adj address and row adj address and (read reads) with word	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 06:49
16	70	(32-bit 4-BIT 8-BIT 16-BIT 64-BIT 128-BIT 256-BIT 512-BIT) adj memory and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 06:49
19	53	(32-bit 4-BIT 8-BIT 16-BIT 64-BIT 128-BIT 256-BIT 512-BIT) adj memory and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:01
20	0	(32-bit AND 16-BIT AND 64-BIT AND 128-BIT) adj memory and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:01
21	5	(32-bit AND 16-BIT) adj memory and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:02
22	5	(32-bit AND 64-BIT) adj memory and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:02
24	0	(8-BIT AND 16-BIT AND 32-bit AND 64-BIT) adj memory and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:02
25	0	(16-BIT AND 32-bit AND 64-BIT AND 128-BIT) adj memory and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:02
23	5	(16-BIT AND 32-bit AND 64-BIT) adj memory and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:05
26	5	(16-BIT AND 32-bit AND 64-BIT) adj WORD and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:05
27	8	(16-BIT AND 32-bit AND 64-BIT) adj WORD and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom sram ram dram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:08

28	5	(16-BIT AND 32-bit AND 64-BIT AND 128-BIT) adj WORD and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom sram ram dram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:08
30	0	(4-BIT AND 8-BIT AND 16-BIT AND 32-bit AND 64-BIT AND 128-BIT) adj WORD and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom sram ram dram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:09
31	0	(256-BIT AND 16-BIT AND 32-bit AND 64-BIT AND 128-BIT) adj WORD and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom sram ram dram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:09
29	5	(8-BIT AND 16-BIT AND 32-bit AND 64-BIT AND 128-BIT) adj WORD and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word AND (rom prom eeprom FLASH eeprom sram ram dram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:10
32	5	(8-BIT AND 16-BIT AND 32-bit AND 64-BIT AND 128-BIT) adj WORD and word and address and word with row and column adj address and row adj address and (read reads) NEAR2 word	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:10
33	279	QUAD ADJ WORD AND DOUBLE ADJ WORD	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:11
34	109496	QUAD ADJ WORD AND DOUBLE ADJ WORD BURST	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:11
35	69	QUAD ADJ WORD AND DOUBLE ADJ WORD AND BURST	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:12
36	39	QUAD ADJ WORD AND DOUBLE ADJ WORD AND BURST AND (rom prom eeprom FLASH eeprom)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:13
37	0	QUAD ADJ WORD AND DOUBLE ADJ WORD AND BURST AND (rom prom eeprom FLASH eeprom) and word with row and column adj address and row adj address and (read reads) NEAR2 word	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:13
38	0	QUAD ADJ WORD AND DOUBLE ADJ WORD AND BURST AND (rom prom eeprom FLASH eeprom) and word with row and column adj address and row adj address	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:13
39	6	QUAD ADJ WORD AND DOUBLE ADJ WORD AND BURST AND (rom prom eeprom FLASH eeprom) and column adj address and row adj address	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 07:13

-	1	"877232"	US-PGPUB	2004/01/07 07:13
-	152	714/747.ccls.	USPAT	2004/01/07 08:37
-	1	"877236"	US-PGPUB	2004/01/07 07:13
-	155	714/747.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 08:37
-	63	714/747.ccls. and (address addresses)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 08:40
-	3	714/747.ccls. and (address addresses) and (data adj (register latch buffer)) and ((address addresses) adj (register latch buffer registers latches buffers))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 08:43
-	17	714/746.ccls. and (address addresses) and (data adj (register latch buffer)) and ((address addresses) adj (register latch buffer registers latches buffers))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 08:43
-	20	714/746,747.ccls. and (address addresses) and (data adj (register latch buffer)) and ((address addresses) adj (register latch buffer registers latches buffers))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 12:06
-	29	(data adj modifier) and (address addresses) and (data adj (register latch buffer)) and ((address addresses) adj (register latch buffer registers latches buffers))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 09:03
-	1	5574926.pn.	USPAT	2004/01/07 11:47
-	1	5938774.pn.	USPAT	2004/01/07 11:44
-	43	"4385351" "5377338" "5938774"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 11:53
-	59	714/6.ccls. and (address addresses) and (data adj (register latch buffer)) and ((address addresses) adj (register latch buffer registers latches buffers))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 12:07
-	31	"5185875" "5623640"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 12:16
-	0	ROM.ti. and word with width with "2.sup"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 14:05

-	1	ROM same (word with width with "2.sup")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 14:06
-	45	ROM.ti. and word with width	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 14:26
-	0	ROM same (address with multiple with 2.sup)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 14:26
-	0	ROM same (address with multiple with "2.sup")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 14:29
-	22	ROM same read same (row with (((low lower) adj order) (least adj significant) LSB) adj2 bits)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 14:43
-	3	(row adj address) same (column adj address) and (width near2 2.sup)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 14:47
-	11	32-bit adj rom	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 14:47
-	9	32-bit adj rom and word and address	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 14:50
-	0	32-bit adj rom and word and address and word with row	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 14:50
-	63	32-bit adj memory and word and address and word with row	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 14:53
-	40	32-bit adj memory and word and address and word with row and column adj address and row adj address	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 06:46